# **Freescale Semiconductor**

MPXC2011DT1 Rev 6, 10/2008

# High Volume Sensor for Low Pressure Applications

Freescale Semiconductor has developed a low cost, high volume, miniature pressure sensor package which is ideal as a sub-module component or a disposable unit. The unique concept of the Chip Pak allows great flexibility in system design while allowing an economic solution for the designer. This new chip carrier package uses Freescale Semiconductor's unique sensor die with its piezoresistive technology, along with the added feature of on-chip, thin-film temperature compensation and calibration.

**NOTE:** Freescale Semiconductor is also offering the Chip Pak package in application-specific configurations, which will have an "SPX" prefix, followed by a four-digit number, unique to the specific customer

### **Features**

- Low Cost
- Integrated Temperature Compensation and Calibration
- Ratiometric to Supply Voltage
- Polysulfone Case Material (Medical, Class V Approved)
- · Provided in Easy-to-Use Tape and Reel

# MPXC2011DT1 MPXC2012DT1

Pressure Sensors 0 to 75 mmHG (0 to 10 kPa)

# **Application Examples**

- · Respiratory Diagnostics
- Air Movement Control
- Controllers
- Pressure Switching

ORDERING INFORMATION							
Device Name	Package Options	Case Pressure Type			Device Marking		
Device Name	Package Options	No.	Gauge	Differential	Absolute	Device Marking	
MPXC2011DTI	Tape and Reel	423A		•		Date Code, Lot ID	
MPXC2012DTI	Tape and Reel	423A		•		Date Code, Lot ID	

## **CHIP PAK PACKAGE**



MPXC2011DT1/MPXC2012DT1 CASE 423A-03

**NOTE:** The die and wire bonds are exposed on the front side of the Chip Pak (pressure is applied to the backside of the device). Front side die and wire protection must be provided in the customer's housing. Use caution when handling the devices during all processes.



# **Pressure**

Freescale Semiconductor's MPXC2011DT1/
MPXC2012DT1 Pressure Sensor has been designed for
medical usage by combining the performance of Freescale
Semiconductor's shear stress pressure sensor design and
the use of biomedically approved materials. Materials with a
proven history in medical situations have been chosen to
provide a sensor that can be used with confidence in
applications, such as invasive blood pressure monitoring. It
can be sterilized using ethylene oxide. The portions of the
pressure sensor that are required to be biomedically
approved are the rigid housing and the gel coating.

The rigid housing is molded from a white, medical grade polysulfone that has passed extensive biological testing including: tissue culture test, rabbit implant, hemolysis, intracutaneous test in rabbits, and system toxicity, USP.

The MPXC2011DT1 contains a silicone dielectric gel which covers the silicon piezoresistive sensing element. The gel is a nontoxic, nonallergenic elastomer system which meets all USP XX Biological Testing Class V requirements. The properties of the gel allow it to transmit pressure uniformly to the diaphragm surface, while isolating the internal electrical connections from the corrosive effects of fluids, such as saline solution. The gel provides electrical isolation sufficient to withstand defibrillation testing, as specified in the proposed Association for the Advancement of Medical Instrumentation (AAMI) Standard for blood pressure transducers. A biomedically approved opaque filler in the gel prevents bright operating room lights from affecting the performance of the sensor.

The MPXC2012DT1 is a no-gel option.

### MAXIMUM RATINGS

Table 1. Maximum Ratings<sup>(1)</sup>

Rating	Symbol	Value	Unit
Maximum Pressure (Backside)	P <sub>max</sub>	75	kPa
Storage Temperature	T <sub>stg</sub>	-25 to +85	°C
Operating Temperature	T <sub>A</sub>	+15 to +40	°C

<sup>1.</sup> Exposure beyond the specified limits may cause permanent damage or degradation to the device.

## **OPERATING CHARACTERISTICS**

**Table 2. Operating Characteristics** ( $V_S = 10 \text{ Vdc}$ ,  $T_A = 25^{\circ}\text{C}$  unless otherwise noted, P1 > P2)

Characteristic	Symbol	Min	Тур	Max	Unit
Pressure Range <sup>(1)</sup>	P <sub>OP</sub>	0	_	10	kPa
Supply Voltage <sup>(2)</sup>	Vs	_	3	10	Vdc
Supply Current	Io	_	6.0	_	mAdc
Full Scale Span <sup>(3)</sup>	V <sub>FSS</sub>	24	25	26	mV
Offset <sup>(4)</sup>	V <sub>off</sub>	-1.0	_	1.0	mV
Sensitivity	ΔV/ΔΡ	_	2.5	_	mV/kPa
Linearity <sup>(5)</sup>	_	-1.0	_	1.0	%V <sub>FSS</sub>
Pressure Hysteresis <sup>(5)</sup> (0 to 10 kPa)	_	_	±0.1	_	%V <sub>FSS</sub>
Temperature Hysteresis <sup>(5)</sup> (+15°C to +40°C)	_	_	±0.1	_	%V <sub>FSS</sub>
Temperature Effect on Full Scale Span <sup>(5)</sup>	TCV <sub>FSS</sub>	-1.0	_	1.0	%V <sub>FSS</sub>
Temperature Effect on Offset <sup>(5)</sup>	TCV <sub>off</sub>	-1.0	_	1.0	mV
Input Impedance	Z <sub>in</sub>	1300	_	2550	Ω
Output Impedance	Z <sub>out</sub>	1400	_	3000	Ω
Response Time <sup>(6)</sup> (10% to 90%)	t <sub>R</sub>	_	1.0	_	ms
Warm-Up	_	_	20	V	ms
Offset Stability <sup>(7)</sup>	_	_	±0.5	_	%V <sub>FSS</sub>

<sup>1. 1.0</sup> kPa (kiloPascal) equals 0.145 psi.

- 2. Device is ratiometric within this specified excitation range. Operating the device above the specified excitation range may induce additional error due to device self-heating.
- 3. Full Scale Span (V<sub>FSS</sub>) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- 4. Offset  $(V_{\rm off})$  is defined as the output voltage at the minimum rated pressure.
- 5. Accuracy (error budget) consists of the following:

Linearity: Output deviation from a straight line relationship with pressure, using end point method, over the specified

pressure range.

Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to

and from the minimum or maximum operating temperature points, with zero differential pressure applied.

Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the

minimum or maximum rated pressure, at 25°C.

TcSpan: Output deviation at full rated pressure over the temperature range of 0 to 85°C, relative to 25°C.

TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85°C, relative to 25°C.

- 6. Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- 7. Offset stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.

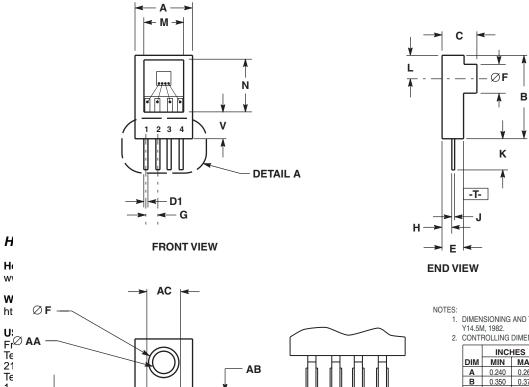
MPXC2011DT1

3

# **PACKAGE DIMENSIONS**

STYLE 2: PIN 1. GND 2. S+ 3. Vs 4. S-

CASE 423A-03 ISSUE C CHIP PAK PACKAGE



AB

D2

**DETAIL A** 

 $\bigcirc$ 

**BACK VIEW** 

- DIMENSIONING AND TOLERANCING PER ANSI
- 2. CONTROLLING DIMENSION: INCH.

	INCI	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.240	0.260	6.10	6.60	
В	0.350	0.370	8.89	9.40	
С	0.140	0.150	3.56	3.81	
D1	0.012	0.020	0.30	0.51	
D2	0.014	0.022	0.36	0.56	
Е	0.088	0.102	2.24	2.59	
F	0.123	0.128	3.12	3.25	
G	0.045	0.055	1.14	1.40	
Н	0.037	0.047	0.94	1.19	
J	0.007	0.011	0.18	0.28	
K	0.120	0.140	3.05	3.56	
L	0.095	0.105	2.41	2.67	
M	0.165	0.175	4.19	4.45	
N	0.223	0.239	5.66	6.07	
٧	0.105	0.115	2.67	2.92	
AA	0.095	0.107	2.41	2.72	
AB	0.015	0.035	0.38	0.89	
AC	0.120	0.175	3.05	4.45	
AD	0.100	0.115	2.54	2.92	

Jŧ Headquarters ARCO Tower 15F 1-8-1, Shimo-Meguro, Meguro-ku, Tokyo 153-0064 0120 191014 or +81 3 5437 9125 support.japan@freescale.com

#### Asia/Pacific:

1-W١

Ει

Fr

T∈ Sc 81 +4

+4 . +4 +3 w۱

AD

Freescale Semiconductor China Ltd. Exchange Building 23F No. 118 Jianguo Road Chaoyang District Beijing 100022 China +86 010 5879 8000 support.asia@freescale.com

#### For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center P.O. Box 5405 Denver, Colorado 80217 1-800-441-2447 or +1-303-675-2140 Fax: +1-303-675-2150 LDCForFreescaleSemiconductor@hibbertgroup.com

provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale™ and the Freescale logo are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners. © Freescale Semiconductor, Inc. 2008. All rights reserved.

